



## Tampa/St. Petersburg IEEE Computer Society and Xilinx Present PLUGFest 2008

Tuesday, November 11, 2008  
9:00AM – 4:00 PM (on site registration opens at 8:30AM)

Holiday Inn Select  
3535 Ulmerton Road  
Clearwater, FL 33762

### Agenda

- 9:00 AM**     ***Xilinx ISE Tools Tips and Tricks***  
This course demonstrates several tools tips and shortcuts that can be used to speed development and achieve timing closure on designs. Covers version 10.1.03i of the Xilinx tools
- 10:00 AM**     ***Accurate Power Estimation Using Xilinx Power Estimation Tool (Avnet)***  
This course covers how to use the XPE tool to generate accurate power estimates of Xilinx designs early in the design cycle.
- 11:00 AM**     ***Achieving Breakthrough Performance with the New PowerPC 440 Processor Architecture***  
Discover the advantages of the embedded PowerPC 440 blocks in the new Virtex5 FXT platform FPGA's. This course covers the differences between the PowerPC405 architecture of the Virtex4 FPGA's and the new PowerPC 440 of the new V5 FXT devices and this dramatically improves the performance of processor based designs.
- 12:00 PM**     ***Xilinx Next Generation Product Roadmap***  
This lunch topic highlights the next generation products from Xilinx from both the high performance and high volume product lines.
- 1:00 PM**     ***GTX/GTP Wizard for High Speed Serial I/O designs***  
Learn how to quickly create High Speed Serial I/O designs for Virtex-5 devices. The Wizard allows the user to configure one or more GTX/GTP tiles by choosing one of the industry standard protocols as a template. Resulting source code design can then be customized as needed or implemented 'as is'. Session will include steps to simulate the design and a 6.5 Gbps demo in hardware.
- 2:00 PM**     ***Designing for Low Power***  
This course covers design techniques that can be used to reduce dynamic power consumption of designs. These power reduction techniques can be applied to any Xilinx product family.
- 3:00 PM**     ***Introduction to the Spartan-3A DSP Video Starter Kit (Nu Horizons)***  
This course covers an introduction to the video starter kit. The Course covers the architecture of the board and how to use System Generator to develop designs that can be run on the Video Starter Kit. Example designs are used to show the development process complete from camera interface to display.

*To register for this event, please visit:*

***<http://time2meet.com/fwcs-cs1/index.html>***

